

0.6-7.125GHz SPDT Switch for High Power Applications

Features

- Broadband frequency range: 0.6 to 7.125GHz
- High power handling capability of up to 40dBm
- Low insertion loss : 0.55dB typical @ 6.0GHz
- High isolation: 25dB typical @ 6.0GHz
- High switching speed: 1 μ s typical
- Low harmonic generation
- Small FCDFN (6-pin, 1.1mm x 0.7mm x 0.37mm) package

Applications

- Multi-Mode GSM/CDMA/WCDMA/LTE and NR including n77, n78, n79 bands
- Cellular modems, tablets and USB Devices
- Other RF front-end modules

General Description

The AW13612HFDR is a single-pole dual-throw switch with high power handling capability of up to 38dBm and low insertion loss. It can be used to support band switching and mode switching for GSM, WCDMA, LTE, and NR applications.

The symmetrical design of internal ports makes it convenient for PCB routing and adjustment of receiving and transmitting signals. The band/mode switching is realized by the GPIO pins as referenced in the chip block diagram and the control logic.

The AW13612HFDR is provided in a compact FCDFN 1.1mm x 0.7mm x 0.37mm-6L package.

Typical Application Circuit

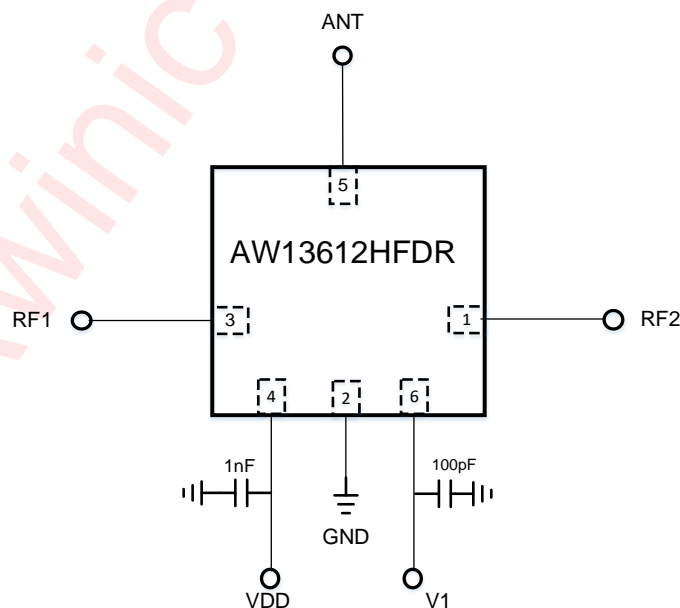
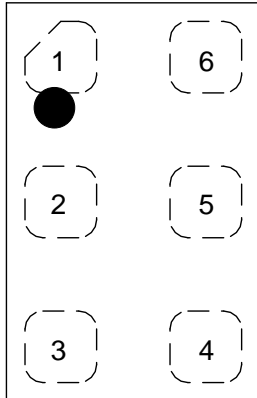


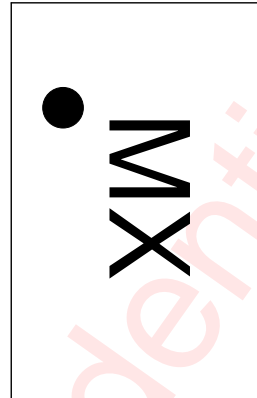
Figure 1 Typical Application Circuit of AW13612HFDR

Pin Configuration And Top Mark

AW13612HFDR
(Top view)



AW13612HFDR Marking
(Top view)



M - AW13612HFDR

X - Production Tracing Code

Figure 2 Pin Configuration and Top Mark

Pin Definition

| No. | NAME | DESCRIPTION |
|-----|------|----------------------|
| 1 | RF2 | RF I/O path 2 |
| 2 | GND | Ground |
| 3 | RF1 | RF I/O path 1 |
| 4 | VDD | DC power supply |
| 5 | ANT | Antenna port |
| 6 | V1 | DC control voltage 1 |

Functional Block Diagram

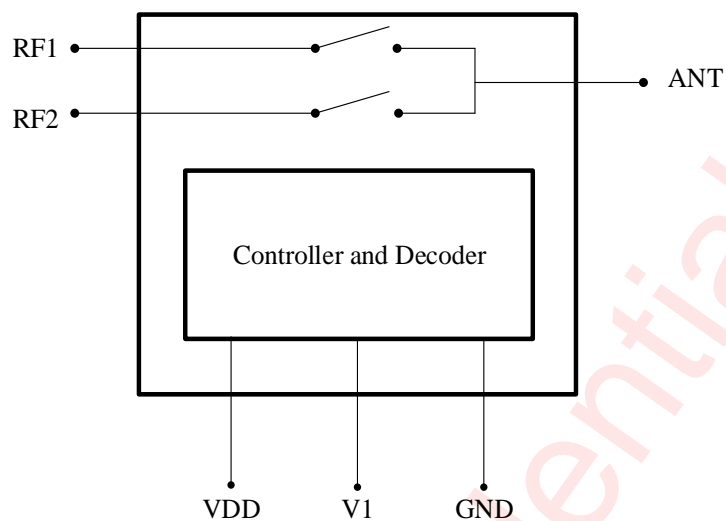


Figure 3 FUNCTIONAL BLOCK DIAGRAM

Ordering Information

| Part Number | Temperature | Package | Marking | Moisture Sensitivity Level | Environmental Information | Delivery Form |
|-------------|-------------|------------------------------|---------|----------------------------|---------------------------|------------------------------|
| AW13612HFDR | -40°C~105°C | FCDFN 1.1mm x 0.7mm-6L | M | MSL1 | ROHS+HF | 9000 units/ Tape and Reel |

Absolute Maximum Ratings^(NOTE1)

| PARAMETERS | | RANGE |
|--|--|----------------|
| Supply voltage range VDD | | -0.3V to 3.6V |
| Control Voltage Range | V1 | 0V to 3.3V |
| RF input power(RF1/RF2) | GMSK, 12.5% Duty cycle, output port VSWR 1:1, +25°C | 40dBm |
| Operating Free-air Temperature Range | | -40°C to 105°C |
| Storage temperature T _{STG} | | -65°C to 150°C |
| Lead temperature (soldering 10 seconds) | | 260°C |
| ESD | | |
| HBM(Human Body Model) ^(NOTE 2) | | ±2000V |
| CDM (Charged Device Model) ^(NOTE 3) | | ±1000V |

NOTE1: Conditions out of those ranges listed in "absolute maximum ratings" may cause permanent damages to the device. In spite of the limits above, functional operation conditions of the device should within the ranges listed in "recommended operating conditions". Exposure to absolute-maximum-rated conditions for prolonged periods may affect device reliability.

NOTE2: The human body model is a 100pF capacitor discharged through a 1.5kΩ resistor into each pin. Test method: ESDA/JEDEC JS-001-2017.

NOTE3: All pins. Test Condition: ESDA/JEDEC JS-002-2018.

Electrical Characteristics

VDD=1.8V, V1=0/1.8V, PIN=0dBm, T=+25°C, Z₀=50Ω. (unless otherwise noted)

| PARAMETER | | TEST CONDITION | MIN | TYP | MAX | UNIT |
|-------------------|--------------------------------|--|-----------|------|------------|------|
| DC Specifications | | | | | | |
| VDD | Supply Voltage | | 1.65 | 1.8 | 3.3 | V |
| IDD | Supply Current | | | 50 | 100 | μA |
| VCTL_H VCTL_L | Control Voltage High Low | | 0.75 0 | 1.8 | VDD 0.4 | V |
| ICTL | Control Current | VCTL = 1.8V | | 0.1 | 1 | μA |
| Tsw | Switching Time | 50% of final control voltage to 90% of final RF power, switching between RF1/2 | | 1 | 2 | μs |
| RF Specifications | | | | | | |
| IL | Insertion loss | 600-960MHz | | 0.24 | 0.35 | dB |
| | | 960-2200MHz | | 0.3 | 0.4 | dB |
| | | 2200-2700MHz | | 0.35 | 0.45 | dB |
| | | 3300-4200MHz | | 0.45 | 0.55 | dB |
| | | 4400-5000MHz | | 0.5 | 0.65 | dB |
| | | 5150-5925MHz | | 0.55 | 0.75 | dB |
| | | 5925-7125MHz | | 0.73 | 0.95 | dB |
| ISO | Isolation | 600-960MHz | 35 | 42 | | dB |
| | | 960-2200MHz | 30 | 34 | | dB |
| | | 2200-2700MHz | 28 | 33 | | dB |
| | | 3300-4200MHz | 22 | 30 | | dB |
| | | 4400-5000MHz | 20 | 28 | | dB |
| | | 5150-5925MHz | 20 | 25 | | dB |
| | | 5925-7125MHz | 18 | 24 | | dB |
| RL | Input return loss | 600-960MHz | 20 | 25 | | dB |
| | | 960-2200MHz | 15 | 20 | | dB |
| | | 2200-2700MHz | 14 | 18 | | dB |
| | | 3300-4200MHz | 12 | 15 | | dB |
| | | 4400-5000MHz | 11 | 13 | | dB |
| | | 5150-5925MHz | 10 | 13 | | dB |
| | | 5925-7125MHz | 10 | 12 | | dB |
| 2fo | Second harmonics | PIN=+34dBm, GSM850/900 | | -51 | -45 | dBm |

| PARAMETER | | TEST CONDITION | MIN | TYP | MAX | UNIT |
|------------------|---|--|-----|------|-----|------|
| | | VSWR=1:1 | | | | |
| | | PIN=+32dBm, GSM1800/1900 VSWR=1:1 | | -55 | -50 | dBm |
| 3fo | Third harmonics | PIN=+34dBm, GSM850/900 VSWR=1:1 | | -64 | -55 | dBm |
| | | PIN=+32dBm, GSM1800/1900 VSWR=1:1 | | -65 | -55 | dBm |
| P _{0.1} | 0.1dB Compression Point (ANT pin to RF1/RF2) | GMSK, 12.5% Duty cycle, output port VSWR=1:1 | | 38.5 | | dBm |

Power ON and OFF Sequence

It is very important that the user adheres to the correct power-on/off sequence in order to avoid damaging the device. The control signal V1 should be set to 0V unless VDD is set in the operating voltage range.

Power ON:

- 1) Apply voltage supply --- VDD
- 2) Set Controls---V1
- 3) Apply RF input

Change switch position from one RF port to another:

- 1) Remove RF input
- 2) Change control voltages V1 to set the switch to desired RF port
- 3) Apply RF input

Power OFF:

- 1) Remove RF input
- 2) Remove control voltages-V1
- 3) Remove VDD input

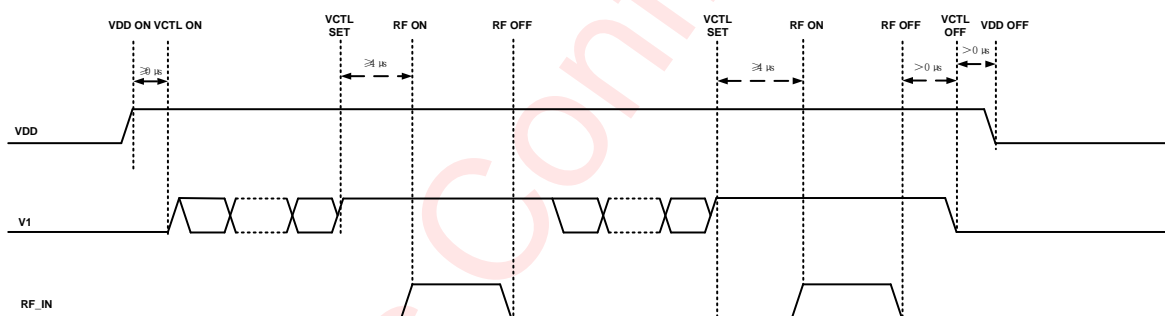
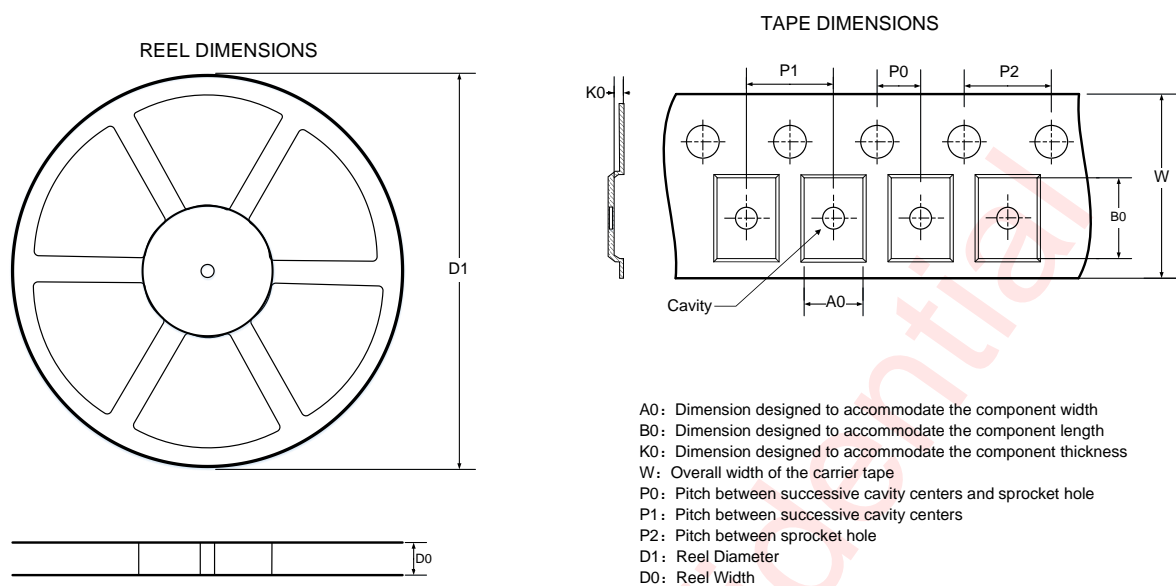


Figure 4 Power on/Change switch/Power off sequence

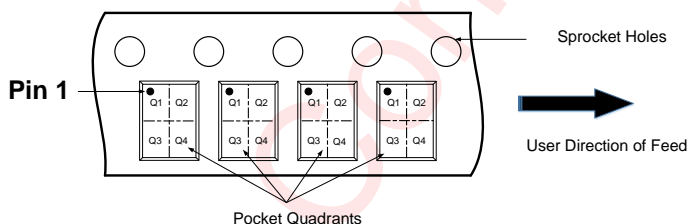
AW13612HFDR Control Logic

| State | Active Path | V1 |
|-------|-------------|----|
| 0 | ANT to RF1 | 0 |
| 1 | ANT to RF2 | 1 |

Tape And Reel Information



QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



Note: The above picture is for reference only. Please refer to the value in the table below for the actual size

DIMENSIONS AND PIN1 ORIENTATION

| D1 (mm) | D0 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P0 (mm) | P1 (mm) | P2 (mm) | W (mm) | Pin1 Quadrant |
|------------|------------|------------|------------|------------|------------|------------|------------|-----------|---------------|
| 178 | 8.4 | 0.82 | 1.22 | 0.46 | 2 | 2 | 4 | 8 | Q1 |

All dimensions are nominal

Figure 5 Tape and Reel

Package Description

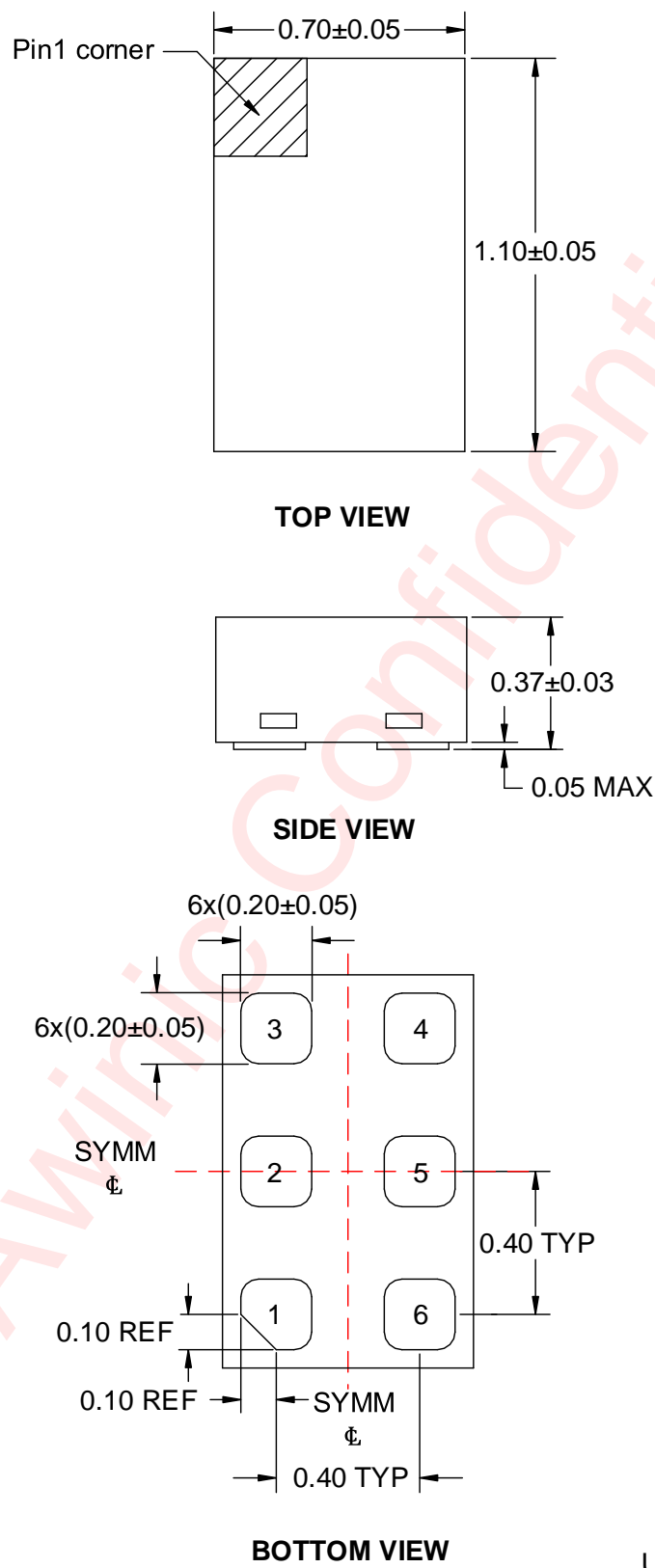
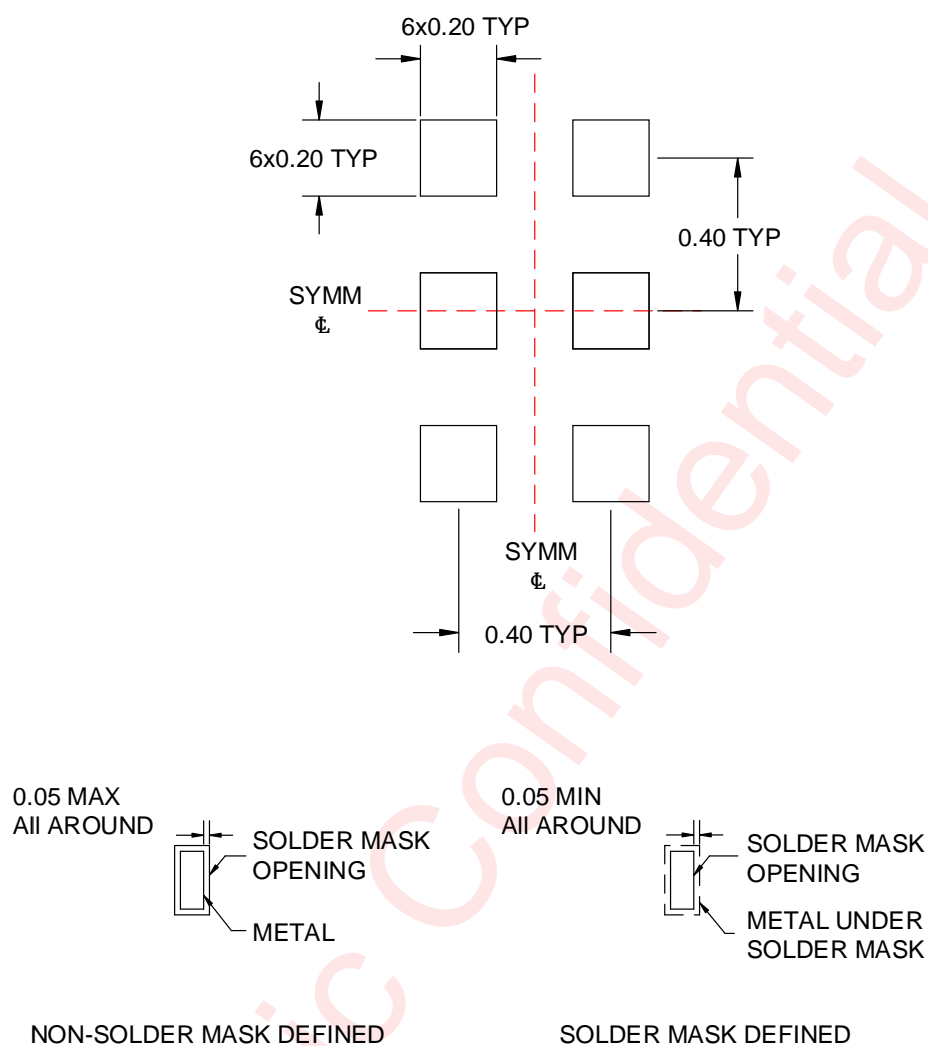


Figure 6 Package Outline

Land Pattern Data



Unit: mm

Figure 7 Land Pattern Data

Revision History

| Version | Date | Change Record |
|---------|-----------|---------------------|
| V1.0 | Apr. 2024 | Officially Released |

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